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TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT (Under 37 CFR 1.97(b) or 1.97(c))				Docket No. 02008/092002	
In Re Application Of: YAMAGUCHI, et al.					
Application No. 10/776,926	Filing Date 02/11/2004	Examiner	Customer No. 22511	Group Art Unit	Confirmation No.
Title: MEASURING APPARATUS AND MEASURING METHOD					
<div></div> <div>Address to: Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450</div> <div>37 CFR 1.97(b)</div> <div>1. <input checked="" type="checkbox"/> The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.</div> <div>37 CFR 1.97(c)</div> <div>2. <input type="checkbox"/> The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:</div> <div><div><input type="checkbox"/> the statement specified in 37 CFR 1.97(e);</div><div>OR</div><div><input type="checkbox"/> the fee set forth in 37 CFR 1.17(p).</div></div>					

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(Under 37 CFR 1.97(b) or 1.97(c))

Docket No.
02008/092002

In Re Application: **YAMAGUCHI, et al.**

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10/776,926	02/11/2004		22511		

MEASURING APPARATUS AND MEASURING METHOD

Payment of Fee

(Only complete if Applicant elects to pay the fee set forth in 37 CFR 1.17(p))

- ☐ A check in the amount of _____ is attached.
- ☒ The Director is hereby authorized to charge and credit Deposit Account No. **50-0591**
as described below.
- ☐ Charge the amount of _____
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Dated: **8/10/04**


Signature

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	ATTY DOCKET NO. 02008/092002	APPLICATION NO. 10/776,926
	APPLICANT(S) T. YAMAGUCHI, et al.	
	FILING DATE 02/11/2004	GROUP ART UNIT

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	A1	5,418,789	05/23/1995	J.e. Gersbach, et al	371	5.2	10/14/1992

U.S. PATENT APPLICATION PUBLICATIONS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

		B1	Digital Line Systems Based on the Synchronous Digital hierarchy for Use on Optical Fibre Cables, ITU-T Recommendation G-958, dated November 1994, 26 pages
		B2	Jitter and wander measruing equipment for digital systems which are abased on the synchronous digital hierarchy (SDH), ITU-T Recommendation 0.172, dated March 1999, 38 pages

EXAMINER	DATE CONSIDERED
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

INFORMATION DISCLOSURE CITATION <i>(Use several sheets if necessary)</i>	ATTY DOCKET NO. 02008/092002	APPLICATION NO. 10/776,926
	T. YAMAGUCHI, et al.	
	FILING 02/11/2004	GROUP ART

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U.S. PATENT APPLICATION PUBLICATIONS							
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							YES	NO

OTHER DOCUMENTS <i>(Including Author, Title, Date, Pertinent Pages, Etc.)</i>			
		B3	SONET OC-192 Transport system Generic Criteria, GR-1377-CORE, Issue 5, dated December 1998, 104 pages
		B4	The control of jitter and wander within digital networks which are based on the synchronous digital hierarchy (SDH) ITU-T recommendation G.825, dated March 2000, 22 pages

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OTHER DOCUMENTS <i>(Including Author, Title, Date, Pertinent Pages, Etc.)</i>			
		B5	E.A. Lee and D.G. Messerschmitt, Digital Communication, 2nd Edition, 5 pages, 1994
		B6	International Search Report, dtaed October 1, 2003, 3 pages

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OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Pages, Etc.)*

		B7	Miller, C.M. et al., "Jitter Analysis of High-Speed Digital Systems", Hewlett-Packard Journal, Hewlett-Packard Co., Palo Alto, US, vol. 46, no 1, 1 February 1995 (1995-02-01), pages 49-56
		B8	Yamaguchi, T.J. et al., "Extraction of peak-to-peak and RMS sinusoidal jitter using an analytic signal method"; VSLI TestSymposium 2000. Proceedings. 18th IEEE Montreal, Que., Canada 30 April-4 May 2000, Los Alamitos, CA, USA, IEEE Comput. SOC, US 30 April 2000, pages 395-402

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OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Pages, Etc.)*

	B9	Baldini J.J. et al. "Jitter in digital transmission systems - characteristics and measurement techniques" Global Telecommunications Conference. Miami, Nov. 29-Dec. 2, 1982, New York, I.E.E.E., US vol 2 conf. 1982, November 1982 (1982-11) pages 658-664
	B10	Yamaguchi, T.J. et al. "Jitter measurements of a power-pc microprocessor using an analytic signal method", International Test Conference 2000, October 3-5, 2000 5 pages

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